Electronic Acknowledgement Receipt				
EFS ID:	1404182			
Application Number:	10620151			
International Application Number:				
Confirmation Number:	8500			
Title of Invention:	Digital clock recovery circuit employing fixed clock oscillator driving fractional delay line			
First Named Inventor/Applicant Name:	Matthew A. Kliesner			
Customer Number:	27975			
Filer:	David L. Stewart/joellen murphy			
Filer Authorized By:	David L. Stewart			
Attorney Docket Number:	72206			
Receipt Date:	28-DEC-2006			
Filing Date:	15-JUL-2003			
Time Stamp:	14:21:36			
Application Type:	Utility			

## Payment information:

Submitted with Payment	no
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## File Listing:

Document Number	Document Description	File Name	File Size(Bytes)	Multi Part /.zip	Pages (if appl.)
1		72206amd.pdf	239618	yes	4

	Multipart Description/PDF files in .zip description			
	Document Description	Start	End	
	Amendment - After Non-Final Rejection	1	1	
	Applicant Arguments/Remarks Made in an Amendment	2	4	
Warnings:		1		

Information:

Total Files Size (in bytes): 239618

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## New Applications Under 35 U.S.C. 111

If a new application is being filed and the application includes the necessary components for a filing date (see 37 CFR 1.53(b)-(d) and MPEP 506), a Filing Receipt (37 CFR 1.54) will be issued in due course and the date shown on this Acknowledgement Receipt will establish the filing date of the application.

## National Stage of an International Application under 35 U.S.C. 371

If a timely submission to enter the national stage of an international application is compliant with the conditions of 35 U.S.C. 371 and other applicable requirements a Form PCT/DO/EO/903 indicating acceptance of the application as a national stage submission under 35 U.S.C. 371 will be issued in addition to the Filing Receipt, in due course.